

VFSTR :: VADLAMUDI
(Deemed to be UNIVERSITY)

**Time Table for Diploma II Year I Semester (R21) Regular End Examination -
November-2024**

(Regular for 2023 batch, Supple for 2022 batch)

Released Date: 15.10.2024

Examination Time: 09.00 A.M. to 12.00 P.M.

| Branch & Date | DCSE | DECE |
|--------------------------|------------------------------------|---|
| 26.11.2024 (Tuesday) | Introduction to DBMS (22DCS201) | Electronic Devices (22DEC202) |
| 28.11.2024 (Thursday) | Python Programming (22DCS202) | Python Programming (22DCS202) |
| 30.11.2024 (Saturday) | OOP Through C++ (22DCS203) | Electric Circuits and Networks (22DEC203) |
| 03.12.2024 (Tuesday) | Software Engineering (22DCS204) | Analog and Digital Communications (22DEC204) |
| 05.12.2024 (Thursday) | Digital Electronics (22DEC201) | Digital Electronics (22DEC201) |
| 07.12.2024 (Saturday) | Mathematics - III (22DMT201) | Mathematics - III (22DMT201) |



Controller of Examinations

Copy to PA to VC, PA to Registrar, DDHs, Training & Placement Cell, Workshop I/C, Bus I/C, Reception, Web Administrator, Library

Controller of Examinations
(Deemed to be UNIVERSITY)

VU/CE/CIR/24/570 (BG) Date: 09.10.2024

