

# GATEWAY TO A LUCRATIVE CAREER IN SEMICONDUCTOR INDUSTRY



**VIGNAN'S**  
Foundation for Science, Technology & Research

## VEDA IIT OFFERS



Learn. Explore. Excel.

### INDUSTRY-READY PROFESSIONAL TRAINING IN **VLSI DESIGN/EMBEDDED SYSTEM DESIGN**

In fulfilment of coursework for 2 Semesters of B.Tech 4<sup>th</sup> Year  
for Vignan University

in collaboration with



VEDA IIT, one of the top VLSI Engineering Institutes, conducts the program

Knowledge-intensive and Industry-Oriented Program

Vignan University approved transfer of 40 credits earned at VEDA IIT for award of B.Tech Degree

Internship in 2<sup>nd</sup> Semester in VEDA IIT / Consortium company\*

\*Terms and conditions apply. For details contact HR, VEDA IIT

#### COMMON ENTRANCE TEST

Preliminary Test(Online): 26<sup>th</sup> July, 2024; 10 AM - 11AM

Main Test(Offline): 4<sup>th</sup> August, 2024; 10 AM - 1 PM

Test Center: Hyderabad

Last Date to apply: 22<sup>nd</sup> July, 2024

Syllabus: visit [www.vedaiit.org](http://www.vedaiit.org)

Apply online at [www.vedaiit.org](http://www.vedaiit.org)

#### ELIGIBILITY & ADMISSION

- Students of Vignan University completing B.Tech. III year by 31<sup>st</sup> August, 2024 in ECE/EEE/EIE/CSE
- Admission: Based on performance in the entrance tests & Interview

## ABOUT VEDA IIT

- Expert faculty from reputed VLSI Design Houses/Institutes
- Projects with proven design methodologies
- Well-equipped infrastructure with high-end state-of-the-art computing facilities
- Excellent placement track record in leading Technology Companies since inception
- Partnership with global leaders of technology
- Innovative Teaching-Company model with Industry and University participation - first of its kind
- Listed among top five institutes in VLSI Engineering by Times of India
- Pioneered in incubating many successful design teams/companies
- Lead organizers of International VLSI Conferences - VLSI 2006, 2008, 2012 & 2017
- Holistic education covering various aspects of VLSI and System Design

Hands-on Experience with best-in-class tools covering the entire ASIC design flow from the world class EDA Vendors

cā dence™

Mentor  
Graphics®

SYNOPSYS®  
Silicon to Software™



Learn. Explore. Excel.

**VEDA IIT, A Unit of The VEDA Educational Society**

Regd. No. 410/2014 | ✉ [hr@vedaiit.org](mailto:hr@vedaiit.org)

Telangana: 2nd Floor, AYDIV IT Park, Puppalaguda, Rangareddy 500 032

Andhra Pradesh: Vidyanagar, 1<sup>st</sup> Lane & 1<sup>st</sup> Cross, Guntur 522 007

HYD: 040 - 43929999 | 📞 +91 9989928276 | 📠 GNT: 0863 - 2374000 | 📠 +91 8328535061