GATEWAY TO A LUCRATIVE CAREER IN SEMICONDUCTOR INDUSTRY

VEDA IIT OFFERS

INDUSTRY-READY PROFESSIONAL TRAINING IN
VLSI DESIGN/EMBEDDED SYSTEM DESIGN

**In fulfillment of coursework for 2 Semesters of B.Tech 4th Year
Vignan University**

in collaboration with

SoCtronics & Other Consortium Companies

VEDA IIT, one of the top VLSI Engineering Institutes, conducts the program
Knowledge-intensive and Industry-Oriented Program
Vignan University approved transfer of 40 credits earned at VEDA IIT for award of B.Tech Degree
Internship in 2nd Semester in VEDA IIT/ Consortium company*

*Terms and conditions apply. For details contact HR, VEDA IIT

COMMON ENTRANCE TEST

➤ **Preliminary Test (Online): 6th June, 2025;
10:30 AM - 11:30 AM**

➤ **Main Test (Offline): 15th June, 2025; 10 AM – 1 PM
Test Center: VEDA IIT, Hyderabad
Last Date to apply: 31st May, 2025**

ELIGIBILITY & ADMISSION

➤ **Students of Vignan University, completing
B.Tech. III year by 11th July, 2025 in ECE/EEE/EIE/CSE**

➤ **Admission: Based on performance in the
entrance tests, Interview and NOC issued by the
colleges / institutes**

**For more information and to apply online
Visit : www.vedaiit.org**

Registration fee : Rs 300/-

No fees for campus students who apply on-line with valid coupon code

VEDA IIT provides a limited number of 'valid coupon codes' to TPO/HODs of different colleges on request to hr@vedaiit.org

ABOUT VEDA IIT

- **Expert faculty from reputed VLSI Design Houses/Institutes**
- **Projects with proven design methodologies**
- **Well-equipped infrastructure with high-end state-of-the-art computing facilities**
- **Excellent placement track record in leading Technology Companies since inception**
- **Partnership with global leaders of technology**

- **Innovative Teaching-Company model with Industry and University participation - first of its kind**
- **Listed among top five institutes in VLSI Engineering by Times of India**
- **Pioneered in incubating many successful design teams/companies**
- **Lead organizers of International VLSI Conferences - VLSI 2006, 2008, 2012 & 2017**
- **Holistic education covering various aspects of VLSI and System Design**

**Hands-on Experience with best-in-class tools covering the entire
ASIC design flow from the world class EDA Vendors**

cadence™   SIEMENS   SYNOPSYS®
Silicon to Software™

VEDA IIT, A Unit of The VEDA Educational Society

Regd. No. 410/2014 | ✉ hr@vedaiit.org

2nd Floor, AYDIV IT Park, Puppalaguda, Rangareddy 500032, Telangana

☎ 040 - 43929999 | 📱 +91 9989928276

Learn. Explore. Excel.