

List of Publications In Conference

1. S. Sivakrishna and Ravi Sekhar, "Design and simulation of 5G massive MIMO kernel algorithm on SIMD vector processor," IEEE International Conference on Signal Processing And Communication Engineering Systems (SPACES), Vijayawada, India, 2018, pp. 53-57. (Scopus - Published)
2. Mubeena Shaik and Ravi Sekhar Yarrabothu, "Comparative Study Of FBMC-OQAM And OFDM Communication System", IEEE International Conference on Trends in Electronics and Informatics (ICOEI 2018), Tirunelveli, Tamil Nadu, INDIA, May 2018 (Scopus – Accepted)
3. Vishnu.S, Sharma.S,Jagatheshkumar.S, Divya .T , "Design of SEPIC using FOPID Tunning with PSO" in proceedings of the 2017CCSCM (CCSCM -2017)Langkawi, Malaysia,4-5 May 2017.
4. T. Divya, P.ArunMozhiDevan, S. Vishnu,"IoT Based Water Purification System using Graphene" in proceedings of the 2017ICETSMI (ICETSMI-2017),Delhi, India,30 April 2017.
5. Joshua Reginald Pullagura and Dhulipalla Venkata Rao "Simulation-Based Comparison of Vampire Attacks on Traditional Manet Routing Protocols" The 6th International Conference on Computer Engineering and Mathematical Sciences (ICCEMS 2017) 7 th-8th December
6. Avireni Srinivasulu, G.Sowjanya, Gautham S. H, T. Pitchaiah and V.V.S.V. Krishna "Operational Transconductance Amplifiers based sinusoidal oscillator with grounded capacitors" Lecture Notes in Electrical Engineering (LNEE) (Springer), Chapter No.: 28, vol. 403, pp. 343-352, ISBN: 978-981-10-2998-1.
7. Kavya Sri and Avireni Srinivasulu " Q-factor and BER Performance Analysis of Inter-satellite Optical Wireless Communication Using 26 Transponders" IEEE International Conference on Intelligent Communication and Computational Techniques, Jaipur, India, Dec 22 - 23rd , 2017.
8. K. Damayanthi, T. Sunil Reddy, B. M. Reddy and Avireni Srinivasulu "Design and Implementation of an Energy Efficient, Low-Cost Robotic Solar Tracker" 3rd International Conference on Nanoelectronics, Circuits & Communication Systems(NCCS-2017) (Springer), Ranchi, India, Nov 11-12th , 2017.
9. J. K. Saini, Avireni Srinivasulu and B. P. Singh "A new low-power full-adder cell for low voltage using CNTFETs " IEEE International Conference on Electronics, Computers and Artificial Intelligence (IEEE ECAI 2017), Targoviste, Romania, 29 June - 01 July, 2017.
10. Arti Ahir, J. K. Saini and Avireni Srinivasulu " A low voltage distinctive source based sense amplifier for memory circuits using FinFETs" International Conference on Smart Innovations in Communications and Computational Sciences (ICSICCS-2017) (Springer), Moga, Punjab, India, Jun 23-24, 2017.
11. Kavya Sri and Avireni Srinivasulu "Performance analysis of Inter-satellite Optical Wireless Communication Using 12 and 24 Transponders" International Conference on

- Microelectronics, Computing & Communication Systems (MCCS-2017) (Springer), Ranchi, India, May 13-14th, 2017.
12. M. Lakshmi Lavanya, Avireni Srinivasulu and V.Venkata Reddy " ZC-CDTA based integrator circuit using single passive component International Conference on Microelectronics, Computing & Communication Systems (MCCS-2017) (Springer), Ranchi, India, May 13-14th, 2017.
 13. R. Linita, V.Venkata Reddy and Avireni Srinivasulu "An integrator circuit using Differential Difference Current Conveyor Transconductance Amplifier" IEEE International Conference on Signal Processing, Communications and Networking (IEEE ICSCN 2017), Chennai, India, March 16-18, 2017.
 14. Shaik Jakeer Hussain " Epileptic Seizure Detection using Wavelets and EMD" IEEE sponsored 4th International Conference on Bio signals, Images and Instrumentation, SSNCE, Chennai , ICBSII 2018 – 22nd-24thMar'18.
 15. Shaik Jakeer Hussain "Epileptic Seizure Prediction based on localization" IEEE sponsored 4th International Conference on Bio signals, Images and Instrumentation, SSNCE, Chennai , ICBSII 2018 – 22nd-24thMar'18.
 16. S. Sivakrishna and R. S. Yarrabothu, "Design and simulation of 5G massive MIMO kernel algorithm on SIMD vector processor," IEEE International Conference on Signal Processing And Communication Engineering Systems (SPACES), Vijayawada, India, 2018, pp. 53-57.
 17. Mubeena Shaik and Ravi Sekhar Yarrabothu, "Comparative Study Of FBMC-OQAM And OFDM Communication System", IEEE International Conference on Trends in Electronics and Informatics (ICOEI 2018), Tirunelveli, Tamil Nadu, INDIA, May 2018 .
 18. Naveen Kumar, P. V. S. M Krishna, R. K. Lal and Avireni Srinivasulu, "Grounded resistance/capacitance controlled multiphase sinusoidal oscillator using differential difference complimentary current conveyors", IEEE International Conference on Communication and Signal Processing - ICCSP'16, April 6-8, 2016.
 19. Sarada Musala and Avireni Srinivasulu, "SelfTesting and Fault Secure XOR/XNOR Circuits Using FinFETS", 5th IEEE International Conference on Communication and Signal Processing (IEEE ICCSP-2016), Melmaruvathur, Tamilnadu, April 6-8, 2016.
 20. V.Vijay and Avireni Srinivasulu" A DCCII Based Square Wave Generator With Grounded Capacitor", 2016 IEEE International Conference on Circuits, Power and Computing Technologies (ICCPCT-2016), Kumarcoil, India.March 18-19, 2016.
 21. Sadulla Shaik, *K Sri Rama Krishna and Ramesh Vaddi*, "Circuit and Architectural Co-Design for Reliable Adder Cells with Steep Slope Tunnel Transistors for Energy Efficient Computing" 29th International Conference on VLSI Design and 15th International Conference on Embedded System (VLSI Design-2016) On January 4-8, 2016.

22. M. S. S. Rukmini, Bajidbi. S and Y. R. Babu, "Implementation of Reversible Arithmetic and Logical Unit and Its BILBO Testing", International Conference on Micro-Electronics, Electro magnetics and Telecommunications, 2016.
23. Syed Zahiruddin and Avireni Srinivasulu, "A High frequency tunable sinusoidal oscillator using single CCCII," The IEEE International Conference On Control Instrumentation Communication and Computational Technologies (ICCICCT- 2015), December 2015.
24. Sadulla Shaik, *K Sri Rama Krishna*, and Ramesh Vaddi, "Tunnel Transistors with Circuit Co-Design in Designing Reliable Logic Gates for Energy Efficient Computing" IEEE Asia Pacific Conference on Post Graduate Research in Microelectronics and Electronics (Prime Asia-2015), November 28-29, 2015.
25. T. Pitchaiah, Sowmya and P. V. Sridevi, "ASIC Realization of FIR Digital Filters-A Comparison Study Of Power, Area And Delay For Various Architectures," International Conference on Electromagnetic Interference and Compatibility-2015, July, 2015.
26. Ravipati Linitha, Avireni Srinivasulu and V.Venkata Reddy, "A Schmitt Trigger Based on DDCCTA without Any Passive Components", IEEE International Conference on Communication and Signal Processing (IEEE ICCSP' 15), April 2-4, 2015.
27. T.Nagateja, T.Venkata Rao and Avireni Srinivasulu, "Three Low Voltage, High Speed FinFET Based 1-BIT BBL-PT Full Adders", IEEE International Conference on Communication and Signal Processing (IEEE ICCSP' 15), April 2-4, 2015, Scopus Cited.
28. Chandra Shaker Pittala and Avireni Srinivasulu, "Two Simple Sinusoidal Oscillators Using Single Operational Transresistance Amplifier", IEEE International Conference on Signal Processing, Communications and Networking (IEEE ICSCN 2015), Chennai, India, March 26-28, 2015, pp. 5 pages, 2015, 978-1-4673-6823-0/15© 2015 IEEE.
29. T. Srivyshnavi and Avireni Srinivasulu, "A Current Mode Schmitt Trigger Based on Current Differencing Transconductance Amplifier: Without any passive components", IEEE International Conference on Signal Processing, Communications and Networking (IEEE ICSCN 2015), Chennai, India, March 26-28, 2015, 4 pages, 2015, 978-1-4673-6823-0/15© 2015 IEEE.
30. Usha Rani Nelakuditi, T. Sri Rama Bharatwaj and Narayana Bhagirath.T, "Novel VLSI Architecture for Real time Medical Image Segmentations", IEEE sponsored International Conference ICECS – 2015, 26-27 Feb 2015.
31. Sk. Apsana Parveen, M. S. S. Rukmini and Avireni Srinivasulu, "Two novel Schmitt trigger circuits with eight transistors:", IEEE International Conference on Signal

- Processing and Communication Engineering Systems (IEEE SPACES-2015), Guntur, India, January 2-3, 2015, pp. 10-14, 2015, DOI:10.1109/SPACES, 2015.7058233.
32. M. S. S. Rukmini and A. Srinivasulu, "Two Multiplexers Realized Using Two New Schmitt Trigger Circuits," DST sponsored International conference, ICEAPEA 2015, January, 2015.
 33. Dr. M. S. S. Rukmini and A. Srinivasulu, "Two New Schmitt Trigger Circuits Based On Current Sink And Current Source Inverters," IEEE International Conference on Signal Processing And Communication Engineering Systems-2015 (SPACES 2015), January, 2015.
 34. Bandreddy Siva Nageswara Rao, Sadulla Shaik and Ramesh Vaddi, "Design and Performance Benchmarking of Hetero-junction Tunnel FET based Differential Amplifier for Ultra-Low Power Applications", IEEE International Conference on VLSI Design and 14th International Conference on Embedded Systems (VLSI Design &ES-2015), 2015.
 35. Taninki Sai Lakshmi and Avireni Srinivasulu, "A Low Power Encoder For A 5-GS/S 5-Bit Flash ADC," IEEE International Conference On Advanced Computing (IEEE ICOAC 2014), December, 2014.
 36. Balakrishna. K, Sarada. M and Avireni Srinivasulu, "High Performance Hardenable Memory Cell Using 7-Transistors," IEEE International Conference On Advanced Computing (IEEE ICOAC 2014), December, 2014.
 37. Chandra Shaker Pittala and Avireni Srinivasulu, "Quadrature Oscillator Using Operational Transresistance Amplifier," IEEE Applied Electronics 2014 International Conference (IEEE AEIC-14), September, 2014.
 38. Adipudi Bala Tripura Sundari, Avireni Srinivasulu and T.N.Bhagirath, "Two Rail-To-Rail Class-AB CMOS Buffers With High Performance Slew Rate And Delay," IEEE International Conference On Devices, Circuits And Communications, September, 2014.
 39. Avireni Srinivasulu, M.S.S.Rukmini, Sarada Musala, M.Praveen Ram and Santashraya Prasad, "Pulse Width Modulator Based On Second Generation Current Conveyor," IEEE International Conference On Devices, Circuits And Communications (IEEE Icdccom-2014), September, 2014.
 40. Kunchala Sivakumari, Avireni Srinivasulu and V.Venkata Reddy, "A High Slew Rate, Low Voltage CMOS Class-AB Amplifier," IEEE Applied Electronics 2014 International Conference (IEEE AEIC-14), September, 2014.
 41. Sarada Musala, B. Raja Shekara Reddy and Mrudula Singamsetti, "Analysis of Low Power Full Adder Based On DPL And Transmission Gates ," International Conference On Innovations In Electronics And Communication Engineering (ICIECE-2014), July, 2014.

42. T. Pitchaiah, M. Dhanalaxmi and P. V. Sridevi, "FPGA Implementation Of Low Area And Delay Efficient Adaptive Filter Using Distributed Arithmetic," International Conference on Advances in Engineering and Technology Research (ICAETR)-2014, August, 2014.
43. Vemireedy Venkatareddy, Pelluri Sambaiah and Saladi Krishna, "Low Power Reactive Feedback CMOS Low Noise Amplifier," International Conference On Communication And Signal Processing (ICCSP 2014), April, 2014.
44. Thottempudi Pardhu, Usha Rani. Nelakuditi and P.Suresh, "Generation And Validation Of Gaussian Noise Using Random Sequence," International Conference On Electronics And Communication Systems (ICECS)-2014 Coimbatore, February, 2014.
45. Gaurav Kaushal, K. Subramanyam, Siva Nageswar Rao, G.Vidya, Radhika Ramya, Sadulla Shaik H. Jeong, S. O. Jung, and Ramesh Vaddi. "Design and Performance Benchmarking of Steep-Slope Tunnel Transistors for Low Voltage Digital and Analog Circuits Enabling Self-Powered SOCs." 11th International Soc Design Conference (ISOCC-2014).
46. Subramanyam, Kasturi, Sadulla Shaik, and Ramesh Vaddi. "Tunnel FET based low voltage static vs dynamic logic families for energy efficiency." VLSI Design and Test, 18th International Symposium on VDAT-14. IEEE, 2014.
47. Vallabhaneni, Harshita, Aditya Japa, Sadulla Shaik, K. Rama Krishna, and Ramesh Vaddi. "Designing energy efficient logic gates with Hetero junction Tunnel fets at 20nm." In Devices, Circuits and Systems (ICDCS), 2014 2nd International Conference on, pp. 1-5. IEEE, 2014.
48. Chandra Shaker Pittala and Avireni Srinivasulu, "A sinusoidal oscillator using single Operational Transresistance Amplifier", presented in proceedings of the 2013 IEEE International Conference on Advanced Computing (IEEE ICoAC 2013), December 18-19, 2013.
49. Ravi Bolimera and M Paul Sandeep, "FPGA Implementation of Least Mean Square Algorithm for Speech Enhancement", at the international conference on Navigational Systems and Signal Processing Applications (NSSP-2013) at University College of Engineering and Technology, Dec, 2013.
50. V.Vijay and Avireni Srinivasulu, "A square wave generator using single CMOS DCCII", Accepted for presentation in proceedings of the 2013 IEEE International SoC Design Conference (IEEE ISOCC-2013), November 17-19, 2013.
51. Sarada Musala and Avireni Srinivasulu, "Three Ex-NOR Gates Using Six MOS Transistors", Accepted for presentation in proceedings of the 2013 IEEE International SoC Design Conference (IEEE ISOCC-2013), November 17-19, 2013.

52. V. V. S. Vijaya Krishna, "VLSI Implementation of High Performance Multiplier and its Performance Evaluation using Xilinx ISE and Cadence", in the proceedings of National Conference on Advances in Communications and Electrical Engineering NCACEE2K13, ISBN 978-93-83038-077, 6th-7th September-2013.
53. V V S Vijaya Krishna, T Pitchaiah, Sk Sadulla and P V Sridevi, "FPGA Implementation Of Low Power Area Efficient Radix8 Multiplier Using Verilog HDL," International Conference On Navigational Systems And Signal Processing Applications (NSSP-2013), September, 2013.
54. Gaddam Sowjanya, Avireni Srinivasulu, Gautham S. Harinarayan, T.Pitchaiah, and V.V.S.Vijaya Krishna, "A Sinusoidal Oscillator Based on Three OTAs and Two Grounded Capacitors", in proceedings of the 2013 IEEE International Conference on Human computer Interactions (IEEE ICHCI'13), pp. 76 (i-iv), August 23-24, 2013.
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56. Usha Rani.N and Srivalli, "Area Efficient Maximum Likelihood VLSI Implemented Viterbi Decoder for Error Correction", 3rd International Conference On RF & Signal Processing Systems RSPS-2013, pp. 123-128, July26-27, 2013.
57. V.V.S. Vijay Krishna, Monisha A., Sk. Sadulla, and Prathiba J, 'Design and Implementation of an automatic Beverages Vending Machine and its performance evaluation using Xilinx ISE and Cadence", 4Th IEEE International Conference on Computing, Communications and Networking Technologies (ICCCNT) – 2013, ISSN Online: 1573-1499, 4th-6th july,2013.
58. Monisha A., Sk. Sadulla, V.V.S. Vijay Krishna and Prathiba. J, "FPGA Implementation of a Finite State Machine based Vending Machine," International Conference on Electrical, Electronics and Computer Science - ICEEC 2013, 8TH June, 2013. ISBN: 978-81-923541-0-3.
59. Balakrishna. K, Avireni Srinivasulu and Sarada. M, "7-T single end and 8-T differential dual-port SRAM memory cells", in proceedings of the 2013 IEEE Conference on Information and Communication Technologies (IEEE ICT-2013), pp. 1243-1246, ISBN: 978-1-4673-5758-6. DOI: 10.1109/CICT.2013.6558291. Scopus Cited. , Apr 11-12, 2013.
60. Krishna Chennakesava Rao.M, Shanmuga Priya.M, Avireni Srinivasulu and Anjaneyulu Badugu, "MEMS-based C-Band tunable 16-array Vertical Cavity Surface Emitting LASER", published in the proceedings of International conference ICGITS 2013, Ref. ID: 3-9-1, pp. 4-6, April-2013.

61. Gogireddy Sravanthi and Avireni Srinivasulu, 'Performance analysis of two Manchester encoders based on TLG's, modified current sink inverter multiplexer and active N-MOS load inverter multiplexer', in proceedings of the IEEE International Conference on Communication and Signal Processing (IEEE ICCSP'13), pp. 1013-1016 ISBN: 978-1-4673-1620-0. DOI: 10.1109/iccsp.2013.6577209. Scopus Cited, Apr 03-05, 2013.
62. Mahendranath. B and Avireni Srinivasulu, "Performance analysis of a new CMOS output buffer", in proceedings of the IEEE International Conference on Circuit, Power and Computing Technologies (IEEE ICCPCT-2013), pp. 752-755, ISBN: 978-1-4673-4920-8. DOI: 10.1109/ICCPCT.2013.6529041. Scopus Cited, Mar 21-22, 2013.
63. P.V.S.M.Krishna, Avireni Srinivasulu, Sanhita.M, Sweta.K and R.K.Lal, "Second generation current conveyor based square wave generator", in proceedings of the International Conference on Innovation in Engineering and Management (ICIEM 2013), pp. 40 (1-4), Feb 22-23, 2013.
64. I. Kavitha Madhuri and Avireni Srinivasulu, 'Two simple Schmitt trigger circuits with 6-MOS transistors', in proceedings of the 2nd International Conference on Smart Technologies for Materials, Communication, Controls, Computing and Energy (ICST-2013), pp. 242-245, ISBN: 978-81-925286-7-0, Jan 3-5, 2013.
65. Usha Rani.Nelakuditi and S Niranjana Reddy, "Novel On-chip Interconnection Structures for Giga-Scale Integration VLSI Ics", International Conference On Communication and Electronics System design-ICCESD", MNIT, Jan, 2013.
66. K. Shravya Reddy and Ramesh Vaddi, "Robust and Ultra low power subthreshold SRAM design using alternative bit cells" IEEE VLSI Design conference, Selected for Design Contest, Jan, 2013.
67. Maisagalla Gopal and Balwinder Raj, "Low Power 8T SRAM Cell Design For High Stability Video Applications", ITSI Transactions on Electrical and Electronics Engineering (ITSI-TEEE), pp. 91-97, ISSN 2320 –8945, 2013.
68. Shaik, Sadulla, and Prathiba Jonnala. "Performance evaluation of different SRAM topologies using 180, 90 and 45 nm technology." Renewable Energy and Sustainable Energy (ICRESE), 2013 International Conference on. IEEE, 2013.
69. K. Annapurna and B. Seetha Ramanjaneyulu, "GPS enabled collaborative sensing in cognitive radio networks for efficient spectrum assignment", International Conference on Recent Trends in Electronics, Information & Communication Technology (RTEICT-16), May 20-21, 2016.
70. P. J. Reginald and D. Venkata Rao, "Simulation based comparison of AODV family Protocols over IEEE 802.15.4 in Mobile ad hoc networks", 2nd IEEE International

conference on Innovations in Information, Embedded and communications systems (ICIIECS-15), March 17-18, 2016.

71. P. J. Reginald and D. Venkata Rao, "Hop Tree based Collaborative Ad hoc On-demand Multicast Routing for IEEE 802.15.4 in Ad-hoc Networks", 3rd IEEE International Conference Advanced computing and Communication systems 2016, January 22-23, 2016.
72. M. S. S. Rukmini, S. Madbushi and R. Raut, "Security Issues in Cognitive Radio: A Review", International Conference on Micro-Electronics, Electro magnetics and Telecommunications, 2016.
73. Ashutosh Kumar Dikshit, Yeswanth Kumar, Bhargava Teja, S. Tiwari, Bipin K Singh, and Praveen Chandra Pandey, " tunability Of Resonator Based Photonic Crystal Switch Y Elliptical Rods," 2015 Workshop on Recent Advances in Photonics (WRAP), IISc Bangalore, December 2015.
74. Ashutosh Kumar Dikshit, Krishna Chennakesava Rao M and Sushmita, Sriman, "2-D Photonic Crystal Based Sensor for Detection of Samples Based on Refractive Index", 11th International Conference on Microwaves Antenna Propagation & Remote Sensing (ICMARS-2015), December 2015.
75. Ashutosh Kumar Dikshit, Krishna Chennakesava Rao M and Sushmita, Sriman, "2-D Photonic Crystal Based Band-Drop Filter", 11th International Conference on Microwaves Antenna Propagation & Remote Sensing (ICMARS-2015), December 2015.
76. B. SeethaRamanjaneyulu and Ms. K. Annapurna "Supporting real time traffic in cognitive radio networks", IEEE conference, 2015, pp. 482-485, DOI: 10.1109/SPACES.2015.7058201
77. M. Siva Srinivasa Rao, "A novel technique to improve the radiation pattern of TSAs by modifying the substrate", IEEE Conference, 2015, DOI:10.1109/SPACES.2015.7058247.
78. M. Siva Srinivasa Rao, Suryachand GD, Krishna Chaitanya.P and N.Srikanta, "vivaldi antenna design for short range and high data 5 pages, 978-1-4673-6823-0/15© 2015 IEEE, 2015.
79. Maheswar Vissa, Krishna Chennakesava Rao. M, Mrudula S, Bore Gowda S B and Viswanath.Vissa, "Energy Efficient Cluster Based Routing Protocol For Wireless Sensor Networks," IEEE International Conference On Control Instrumentation Communication And Computational Technologies (ICCICCT-2014), July, 2014.
80. N. Ananda Rao, Anjaneyulu Badugu, Satish Kanapala, T. V. Rama Krishna and K Vijaya Vardan, "Design Of Dual Director Short Back Fire Antenna For UWB Applications," Second International Conference On Devices, Circuits And Systems (ICDCS14), International March, 2014.

81. M. Siva Srinivasa Rao, Ch.Raghava Prasad and P.V.V.Kishore, 'Recognizing gestures of sign language using feed forward neural networks', in the proceedings of National Conference on Advanced signal processing applications NCASPA-2103 organised by KL University, 20th-21st December 2013.
82. M. Siva Srinivasa Rao, P.Krishna Chaitanya, T.V.Ramakrishna and N.Srikanta, "performance of a novel substrate integrated waveguide (SIW) band pass filter", in the 2nd National Conference on VLSI, signal processing and communications (NCVSComs-2013) organised by VFSTR University, pp. 15-16, 11-12th december, 2013.
83. V. Harsha Vardhan, K. Annapurna and B. Seetha Ramanjaneyulu, "Priority Based Channel Allocations In Cognitive Radio Networks,"National Conference on Innovations in Engineering and Technology-2013, December, 2013.
84. K. Annapurna, V. Harshavardhan and B. Seetha Ramanjaneyulu, "Opportunistic Spectrum Access For Prioritized Secondary Users In Cognitive Radio Networks," International Conference On Navigational Systems & Signal Processing Applications-2013, International December, 2013.
85. K. Mani Kanta Kumar, K. Annapurna and B. Seetha Ramanjaneyulu, "Revenue to Spectrum Allottes Through Efficient Allocation Of Vacant Channels To Cognitive Radio Users,"National Conference on VLSI, Signal Processing and Communications-2013, December, 2013.
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92. John William Carey Medithe and Usha Rani Nelakuditi, “The Study of Normal and Abnormal EEG”, International Conference on Advanced computing & Communication Systems (ICACCS 2016), January 22-23, 2016.
93. John William Carey Medithe and Usha Rani Nelakuditi, “Removal of Ocular Artifacts in EEG” 10th International Conference on Intelligent Systems (ISCO 2016), January 07-08, 2016.
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95. Dr. J. Mohan, Dr. V. Krishnavani and Yanhvi Guo, ‘Automated Brain Tumor Segmentation on MR Images Based on Neutrosophic set Approach”, IEEE sponsored International Conference ICECS – 2015, 26-27 Feb, 2015.
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102. Y. Ravi Sekhar, “Android Based Health Care Monitoring System”, IEEE Conference, pp. 707-711, 2015, ISBN:978-1-4-4799-6816-9.
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- PIC16F877A”, IEEE Sponsored 9th International Conference on Intelligent Systems and control (ISCO) 2015.
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 105. G. S. R. Satyanarayana and V. Vijayakumar, “Wireless Multi-Flow Sensor Water Consumption Monitoring System Using Zigbee For Gated Communities,” International Conference On Navigational Systems And Signal Processing Applications International, December, 2013.
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